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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Patent Application of |) | • |
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| Lars BOHLIN |) Group Art Unit: 2184 | RECENT |
| Application No.: 09/590,172 |) Examiner: Unassigned | RECEIVED |
| Filed: June 9, 2000 |) | APR 0 4 2001 |
| For: A METHOD OF SUPERVISING PARALLEL PROCESSES | | Technology Center 2100 |

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

| [X] | No additional fee for submission of an IDS is required. | | | |
|-----|---|--|--|--|
| [] | The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed. | | | |
| [] | A certification under 37 C.F.R. § 1.97(e) is also enclosed. | | | |
| [] | A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed. | | | |
| [] | Charge \$ to Deposit Account No. 02-4800 for the fee due. | | | |
| [] | A check in the amount of \$ is enclosed for the fee due. | | | |
| | | | | |

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

By:

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

Date: April 3, 2001

Steven M. duBois Registration No. 35,023



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re P | atent Application of |) | | | |
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| Applica | ation No.: 09/590,172 |) | examiner: | Unassigned | RECEIVED |
| Filed: | June 9, 2000 |) | | | APR 0 4 7001 |
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant hereby submits the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

U.S. Patent No. 4,371,754U.S. Patent No. 4,700,292U.S. Patent No. 4,819,232EP 0 752 656 A2

W.W. Wesley Peterson and E.J. Weldon, Jr. "Error Correcting Codes", MIT Press, Cambridge, 1972"

C.L. Chen and M.Y. Hsiao "Error-Correcting Codes for Semiconductor memory Applications: A State-of-the-Art Review, IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 124-134.

J. Arlat and W.C. Carter "Implementation and Evaluation of a (b,k) - Adjacent Error-Correcting/Detecting Scheme for Supercomputer Systems", IBM Journal of Research and Development, Vol. 28, No. 2, March 1984, pp. 159-169

Copy of International-Type Search Report

Information Disclosure Statement Application No. 09/590,172 Attorney's Docket No. 040060-113 Page 2

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

By:

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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Steven M. duBois

Registration No. 35,023

Technology Center 2100

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

Date: April 3, 2001

| THE THE TION DISCLOSURE | | | SURE | ATTORNEY'S DKT NO. 040060-113 | | APPLICATION NO. 09/590,172 | | | |
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| | | | | FILING DATE June 9, 2000 | | 2184 | | | |
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| EXAMINER'S INITIALS | PATENT NO. | DATE | | NAME | CLASS | SUBCLASS | FILING | DATE | |
| | 4,371,754 | 02/01/83 | De et al. | | | | | | |
| | 4,700,292 | 10/13/87 | Campanini | | | | | | |
| | 4,819,232 | 04/04/89 | Krings | | | REO | | | |
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| INITIALS | 0752656A2 | 01/08/97 | EP | CONTRY | CLASS | SOBCEASS | 165 | 140 | |
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| | C.L. Chen and | M.Y. Hsiao Art Review, | "Error-Correc IBM Journal o | cting Codes for Ser of Research and De | niconduct velopmen | tor memory A t, Vol. 28, N | Applicat o. 2, M | ions: (arch | |
| | Correcting/De | tecting Schen | ne for Superco | n and Evaluation of mputer Systems", 1 4, pp. 159-169 | | | | | |
| EXAMINER | | | 1 | DATE CONSIDERE | D | | | | |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.